

Application No.: 09/940,544  
Supplemental Amendment dated July 29, 2005  
Reply to final Office Action dated March 22, 2005

Docket No.: 8733.497.00-US

**Listing of the Claims:**

1. (Currently Amended) An in-plane switching mode liquid crystal display device comprising:
  - first and second substrates;
  - a liquid crystal layer between the first and second substrates;
  - gate and data lines arranged to cross each other on the first substrate to define a plurality of pixel regions;
  - a plurality of common electrodes and data electrodes on the first substrate, wherein the common and data electrodes apply an electric field to the liquid crystal layer that is parallel to the first substrate; and
  - at least one dummy pattern overlapping at least one portion of the data lines for repairing an open region of the data lines,  
wherein the portion of the data lines overlapped with the dummy pattern is electrically connected to the dummy pattern,  
wherein the dummy pattern is integral with at least one of the common electrodes and is of a same material as the common electrodes.
2. (Original) The device as claimed in claim 1, further comprising a gate insulating film between the data line and the dummy pattern.
3. (Original) The device as claimed in claim 1, wherein the dummy pattern overlaps first and second portions of the data line.
4. (Cancel)
5. (Original) The device as claimed in claim 1, wherein the dummy pattern includes a material the same as that of the common electrode.
6. (Original) The device as claimed in claim 1, wherein the dummy pattern includes a transparent conductive material.
7. (Original) The device as claimed in claim 1, wherein the common electrode includes a transparent conductive material.

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8. (Original) The device as claimed in claim 1, further comprising a common line in parallel to the gate lines.

9. (Original) The device as claimed in claim 8, wherein the common line is electrically connected with the plurality of common electrodes.

10. (Cancelled)

11. (Previously Presented) The device as claimed in claim 4, wherein a portion of the at least one of the common electrodes integral with the dummy pattern is electrically insulated from the common line.

12. (Cancelled)

13. (Cancelled)

14. (Cancelled)

15. (Cancelled)

16. (Cancelled)

17. (Cancelled)

18. (Cancelled)

19. (Cancelled)

20. (Currently Amended) An in-plane switching mode liquid crystal display

device comprising:

first and second substrates;

a liquid crystal layer between the first and second substrates;

gate and data lines arranged to cross each other on the first substrate to define a plurality of pixel regions;

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a plurality of common electrodes and data electrodes on the first substrate wherein the common and data electrodes apply an electric field parallel to the liquid crystal layer that is parallel to the first substrate; and

at least one dummy pattern overlapping at least one portion of the data lines for repairing an open region of the data lines,

wherein the dummy pattern is integral with at least one of the common electrodes and is of a same material as the common electrodes,

wherein a portion of the at least one of the common electrodes integral with the dummy pattern is electrically insulated from the common line.

21. (Previously Presented) The device as claimed in claim 20, further comprising a gate insulating film between the data line and the dummy pattern.

22. (Previously Presented) The device as claimed in claim 20, wherein the dummy pattern overlaps first and second portions of the data line.

23. (Previously Presented) The device as claimed in claim 20, wherein the dummy pattern includes a material the same as that of the common electrode.

24. (Presently Presented) The device as claimed in claim 20, wherein the dummy pattern includes a transparent conductive material.

25. (Previously Presented) The device as claimed in claim 20, wherein the common electrode includes a transparent conductive material.

26. (Previously Presented) The device as claimed in claim 20, further comprising a common line in parallel to the gate lines.

27. (Previously Presented) The device as claimed in claim 26, wherein the common line is electrically connected with the plurality of common electrodes.